

Figure 1

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graph TD
    Antenna[45] --- Duplexer[46]
    Duplexer -- 61 --> Transmitter[60]
    Duplexer -- 47 --> Receiver[48]
    Transmitter -- 59 --> Modulator[57]
    Modulator -- 65 --> Microphone[64]
    Microphone -- 64 --> Modulator
    Modulator -- 58 --> CPU[50]
    CPU -- 62 --> Duplexer
    CPU -- 55 --> Display[56]
    CPU -- 54 --> Keypad[53]
    Receiver -- 49 --> Demodulator[51]
    Demodulator -- 52 --> CPU
    Demodulator -- 67 --> Speaker[66]
    Speaker -- 66 --> Demodulator

```

FIG. 2

The diagram illustrates the timing structure of a DVB-T signal burst. It consists of the following segments in microseconds (μs):

- Ramp-Up:** 10 μs
- FREQUENCY CORRECTION TRAINING:** 272 μs
- TIMING CORRECTION TRAINING:** 256 μs
- BCH PREAMBLE:** 16 μs
- BROADCAST (BCH) INFORMATION:** 512 μs
- Ramp-Down:** 10 μs
- Interburst Guard:** 14 μs

The total duration of the signal burst is 1090 μs.

Fig. 3

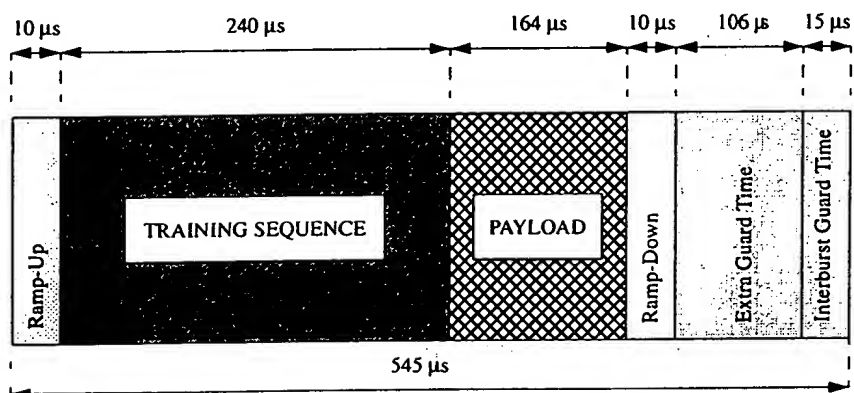


Fig. 4

Diagram illustrating a frame structure 500. The frame is divided into two main sections: a Roundtrip Delay Margin Window 520 and a Beamformer Analysis Window 530. The Roundtrip Delay Margin Window 520 contains packets P₁ through P₉. The Beamformer Analysis Window 530 contains a Marker M. The packets P₁ through P₉ are labeled with their corresponding packet numbers. The Marker M is labeled with 'M'. The frame is also labeled with a sequence of packet numbers: [x₁, x₂, x₃, x₄, x₅, x₆, x₇, x₈, x₉, x₁₀, x₁₁, x₁₂] for the Roundtrip Delay Margin Window 520 and [-x₁, -x₂, -x₃, -x₄, -x₅, -x₆, -x₇, -x₈, -x₉, -x₁₀, -x₁₁, -x₁₂] for the Beamformer Analysis Window 530.

Fig. 5

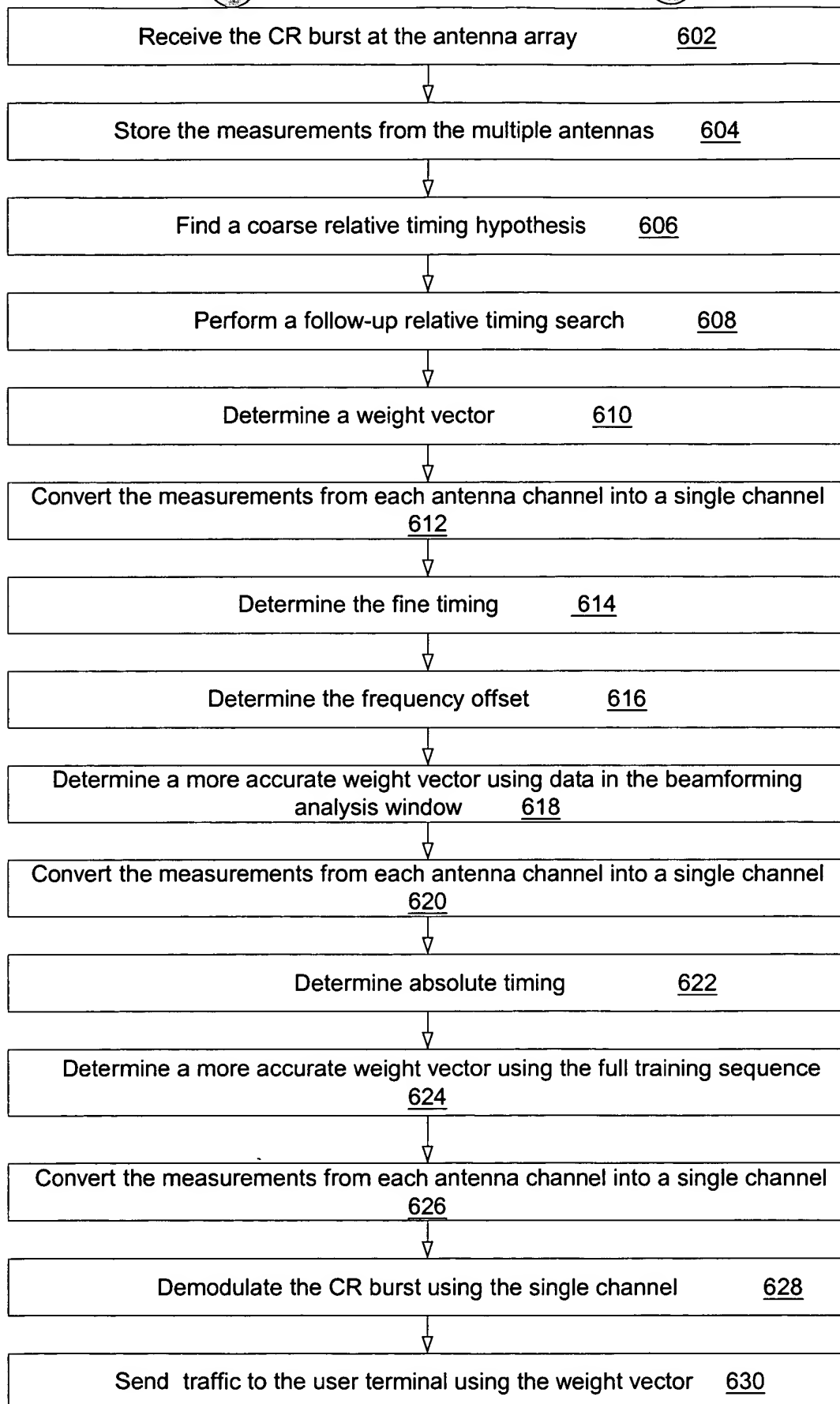
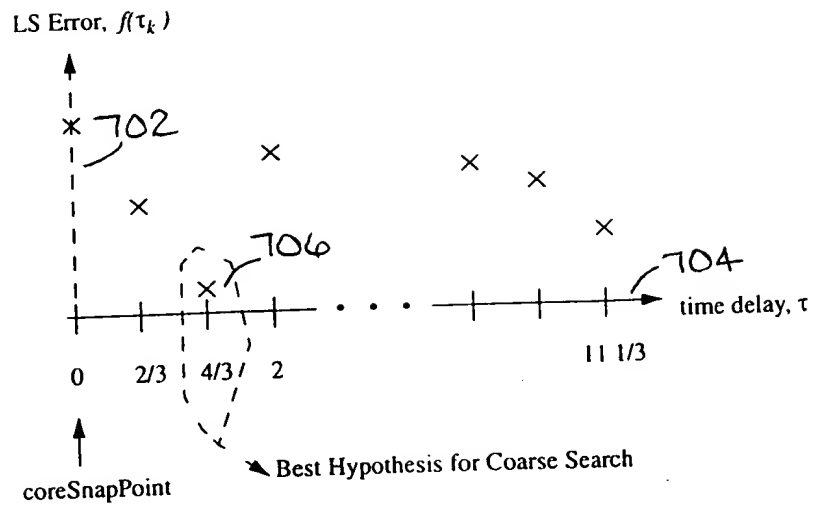


Figure 6



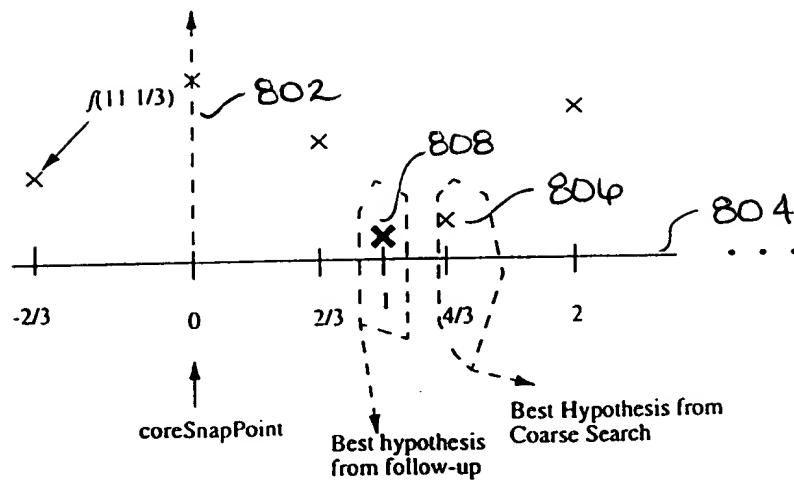


Fig. 8

SECRET